



The Circuit is shown for One Anode Drive and one Segment Drive for Display. In similar fashion connect rest of 5 anodes and 7 segments and one dp decimal point.

The +5 V of 74145BC or 74147BC must be directly from regulator with a big cap or even a separate supply.

IC1 74145 Pin 11 = 5V, Pin 1 = 0V, Pin 24 digital GND, add a decoupling 100 nF cap from +5 to digital GND (see also GND)

IC2 74147 Pin 11 = 5V, Pin 1 = 0V, Pin 24 digital GND, add a decoupling 100 nF cap from +5 to digital GND (see also GND)

This circuit is only a design concept, it is not tested, it could have errors.

use 7414749BC chips in place of 74145BC for only 5V and high speed designs. Use Shrink sleeves and proper gauge of wire. put 100 nF cap for all ICs from positive to negative close to IC, even if omitted in circuit, for example in dual supply two caps around inputs of logic and optoisolators, put up or down to avoid excitations and noise. correct supply of all chips if not mentioned. "analog ground" and "digital ground" must be linked at power supply only, avoid loops, let grounds radiate from a ground plane. use MFR 1% for all Resistors. 33K means 33 kOhms, 22K means 22 kOhms, 1M is 1 megohm, .1T is means ten ten-thimpos. 100 nF is 47 with 4 zeros, 1000 pF, 47000 pF, 470 nF, 0.47 uF, no units like "pf" is plastic, low leakage multilayer.



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